



UNIVERSITY OF MUMBAI  
OFFICE REGISTER FOR THE B.E. ( ELECTRONICS ENGINEERING) (SEM VII) (REVISED COURSE) EXAMINATION HELD IN MAY 2017  
COLLEGE/CENTRE : 390 RSCE AUGUST 2, 2017

SEAT NO.	NAME OF CANDIDATE CENTRE	PP1			PP2			PP3			PP4			PP5			PP6		TOTAL	RESULT
		WR	TW	O/P	WR	TW	O/P	WR	TW	O/P	WR	TW	O/P	WR	TW	O/P	TW	O/P		
		100	25	25	100	25	25	100	25	25	100	25	25	100	25	25	25	25	800	
		40	10	10	40	10	10	40	10	10	40	10	10	40	10	10	10	10		
01.	VLSI DESIGN																			
02.	FILTER DESIGN																			
03.	POWER ELECTRONICS AND DRIVES																			
04.	COMMUNICATION NETWORKS																			
05.	ELECTIVE-I : 8:DIGITAL IMAGE PROCESSING DESIGN																			
06.	PROJECT-A																			
50170017	KHAN MOHAMMED TABISH ISRA R HAMIDA	40	21+	19+	40+	20+	18+	40+	22+	18+	42+	16+	17+	42+	17+	22+	21+	18+	433	P RLE
50170018	MASTEKAR SHANKAR RAGHUNATH ROHINI	48E	19+	20+	AA	20+	18+	42E	20+	17+	42+	16+	17+	44+	17+	17+	19+	20+	396	F
50170019	ORDU KAILASH MARKAND GANGA	44+	22+	17+	40+	15+	20+	AA	15+	16+	40+	16+	19+	56+	16+	18+	17+	16+	387	A ABS
50170020	TAMBE PRASHANT PRABHAKAR LATA	21F	18+	20+	40+	21+	20+	50E	21+	17+	47+	17+	18+	52+	17+	20+	21+	19+	439	F
50170021	TAMBE SUMIT MAHENDRA MOHINI	41+	20+	20+	42+	21+	22+	40	21+	20+	55+	18+	18+	40+	19+	21+	21+	19+	458	P RLE

#:O.229; @:O.5042; \*:O.5045;RCC:O.5050; +:MARKS CARRIED; /:FEMALE;NULL:NULL & VOID AS NO MARKS IN T.W.;P:PASSES;F:FAILS;A:ABSENT;  
--:NOT APPLICABLE;E:EXEMPTION CAN BE CLAIMED; RR:RESERVED; ADC:ADMN. CANCELLED; PPR: PASSED PREVIOUSLY; RLE- LOWER EXAM NOT CLEAR;  
~ : DYSLEXIA BENEFIT